

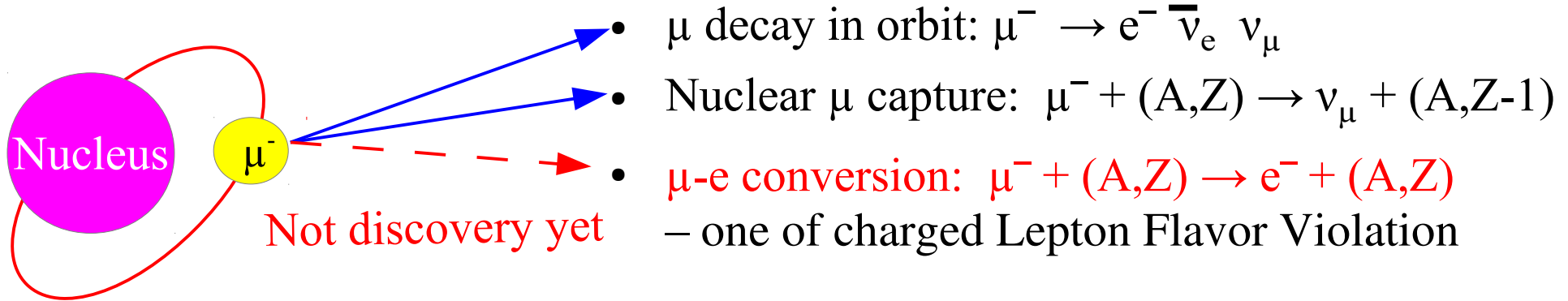
Research Experiences at DeeMe Experiment

Nguyen Minh Truong,

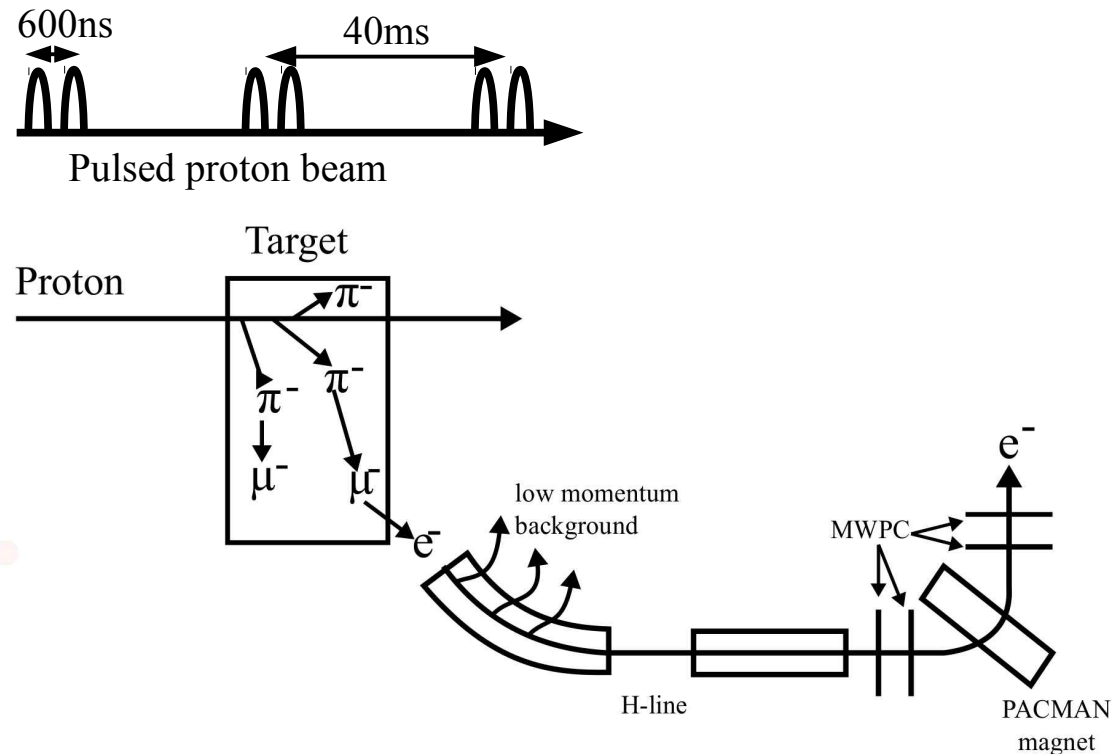
Sep. 14th 2017

Physics Motivation and DeeMe Exp.

1. When muon enters material

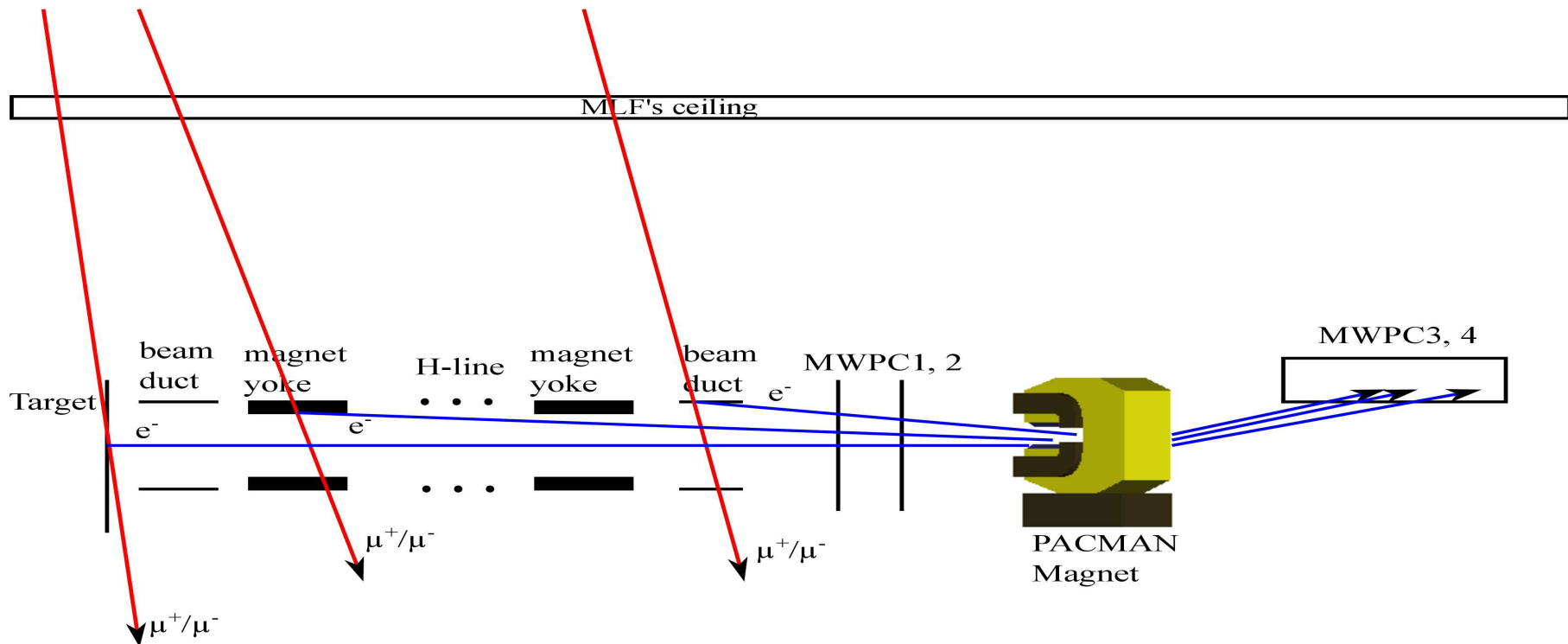


2. DeeMe Experiment

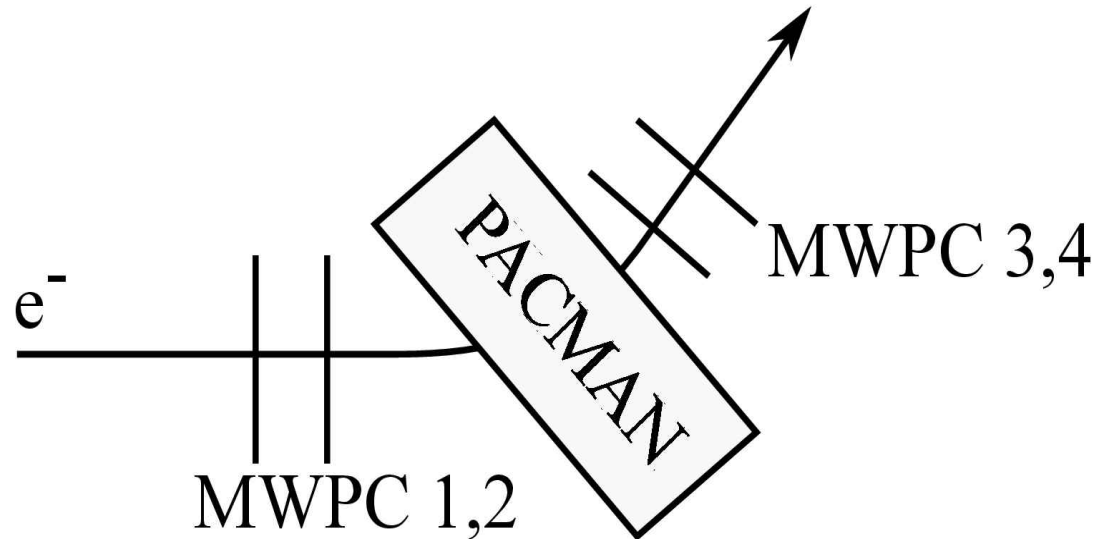


Estimate the Cosmic Rays Induced Background by MC simulation

1. One of potential background for DeeMe exp.
2. MWPCs are placed in horizontal
→ take coincidence of 4MWPCs to remove the most of cosmic ray background.
3. Only background induced from cosmic rays in horizontal direction



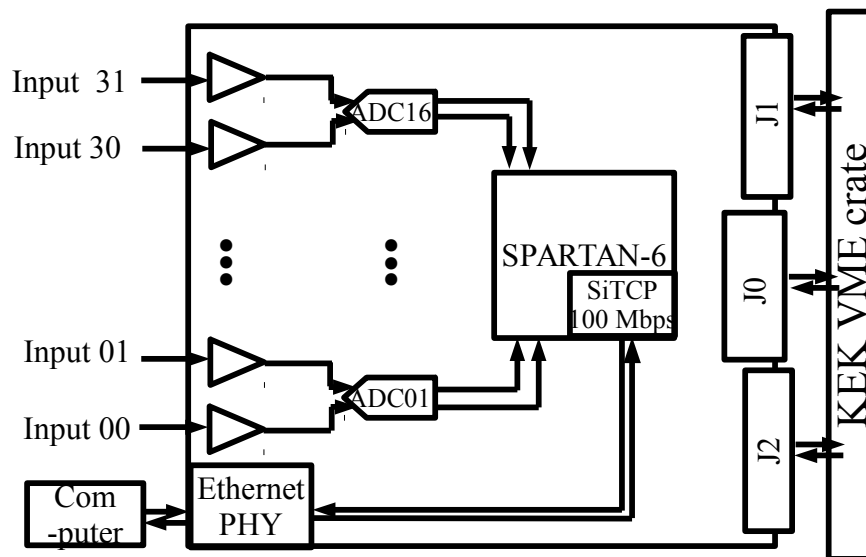
FPGA-Based Data Compression and DAQ System



1. Record signal from MWPC for DeeMe experiment
2. Monitor cosmic rays induced background
→ time window for recording signal $>170 \mu\text{s}$

Hardware & Issue of FADC Board

- 10-bits 100-MHz FADC board, hardware was developed by IGARASHI Youichi for TREK experiment



Issue

- The maximum waveform length can be record $\sim 81.9 \mu\text{s}$
- Deadtime 50 ms

Solve the issue

- Record 2 times of maximum waveform
- Reduce the deadtime

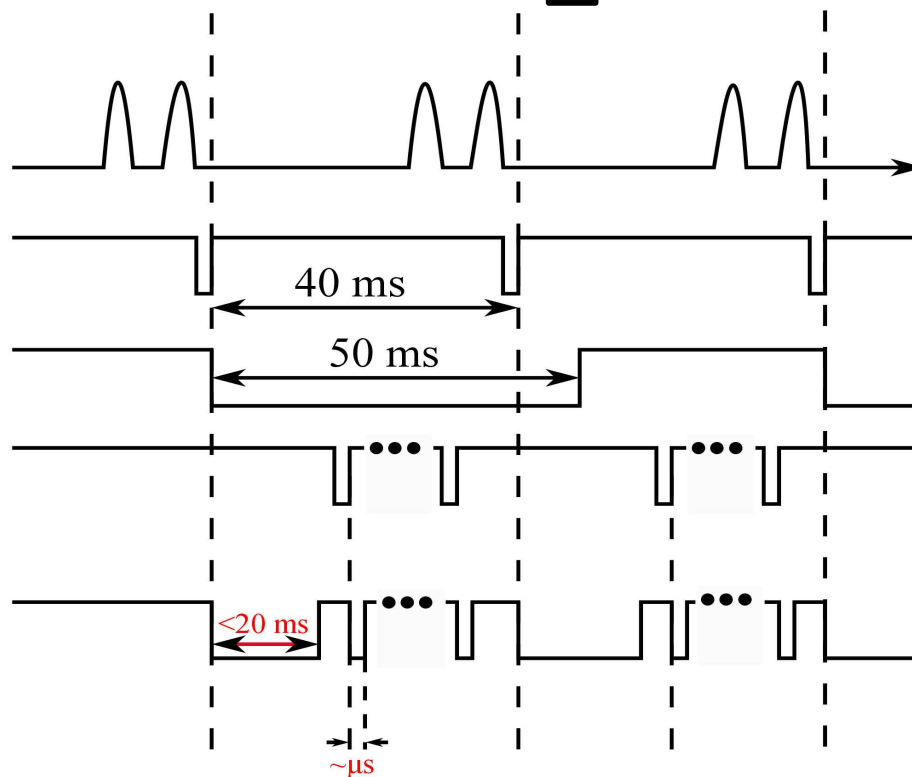
pulsed
Proton beam

Trigger for
beam data

Current
deadtime

Trigger for
cosmic ray

Need
deadtime



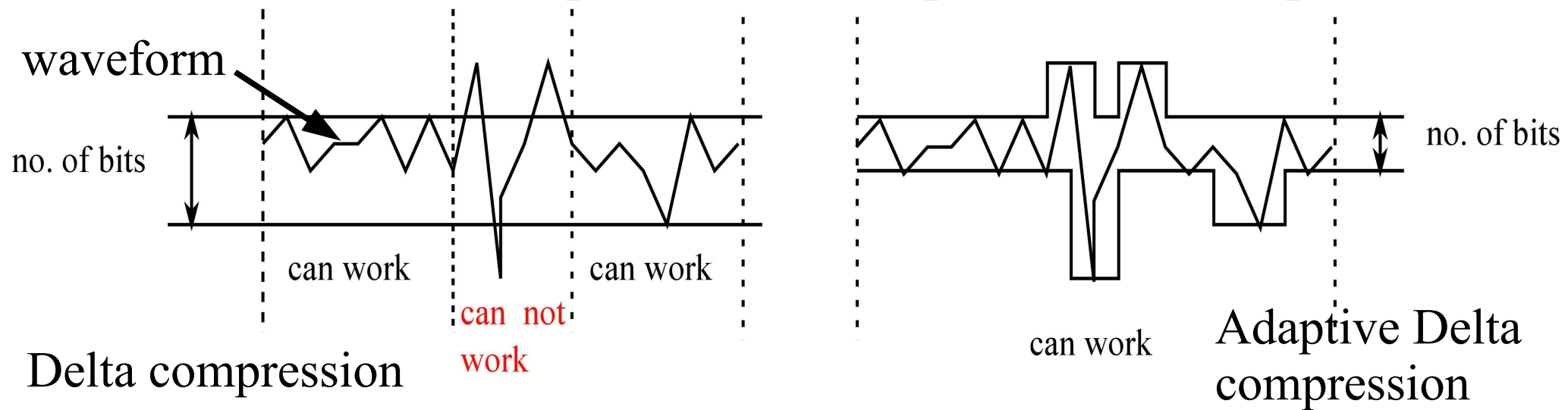
Reduce Deadtime → New Algorithm

“Adaptive Delta Compression Algorithm”

Delta $\Delta_{\text{ADC}} = \text{ADC}_n - \text{ADC}_{n-1}$

1. Lossless compression
2. Monitor noise level of waveform
3. Automatic decides the number of bits to encode the delta value
4. If delta size of any points is larger than the decided number of bits, use again raw data for only those data

Delta compression vs Adaptive Delta compression



DAQ, Data Transfer and Network

Using high performance network switch to solve the head of line block problem

DAQ screen base on MIDAS



14 FADC boards

MIDAS experiment "deeme"			Thu Feb 16 19:30:15 2017 Refr:3		
<input type="button" value="Stop"/> <input type="button" value="Pause"/> <input type="button" value="ODB"/> <input type="button" value="CNAF"/> <input type="button" value="Messages"/> <input type="button" value="ELog"/> <input type="button" value="Alarms"/> <input type="button" value="Programs"/> <input type="button" value="History"/> <input type="button" value="Config"/> <input type="button" value="Help"/>					
Trigger Scaler event					
Run #5146	Running	Alarms: On	Restart: No	Data dir: /home/deeme/online/data2	
Start: Thu Feb 16 19:23:33 2017			Running time: 0h06m42s		
Equipment	FE Node	Events	Event rate[/s]	Data rate[kB/s]	Analyzed
Trigger	kekvm@dmdaq01	1	0.0	0.0	N/A
Scaler	kekvm@dmdaq01	0	0.0	0.0	N/A
SiTCP_board17	feSiTCP17@dmdaq01	10011	25.0	2660.7	N/A
SiTCP_board07	feSiTCP07@dmdaq01	10011	25.0	2660.6	N/A
SiTCP_board16	feSiTCP16@dmdaq01	10011	25.0	2660.7	N/A
SiTCP_board04	feSiTCP04@dmdaq01	10011			
SiTCP_board02	feSiTCP02@dmdaq01	10011			
SiTCP_board12	feSiTCP12@dmdaq01	10011			
SiTCP_board03	feSiTCP03@dmdaq01	10011			
SiTCP_board15	feSiTCP15@dmdaq01	10011			
SiTCP_board05	feSiTCP05@dmdaq01	10011	25.0	2660.7	N/A
SiTCP_board06	feSiTCP06@dmdaq01	10011	25.0	2660.6	N/A
SiTCP_board10	feSiTCP10@dmdaq01	10011	25.0	2660.6	N/A
SiTCP_board11	feSiTCP11@dmdaq01	10011	25.0	2660.6	N/A
EB	Ebuilder@dmdaq01	10019	25.0	34535.8	N/A
SiTCP_board18	feSiTCP18@dmdaq01	10011	25.0	2660.9	N/A

DAQ system can run with 25Hz and 12 FADC boards



High performance network switch
Cisco Catalyst WS-C3850-24T-S

Summary and Discussion

1. Cosmic ray induced background is estimated for DeeMe exp.
~ 0.025 background/det. liver year
→ analysis time window 10 μ s

to achieve the 3σ significance => record the waveform > 170 μ s
2. Build new firmware for FADC readout board
+ develop the new compression algorithm “adaptive delta compression”
+ Read out long data waveform ~ 80 μ s
+ Small dead time ~ 18ms, maximum trigger rate is 50 Hz
→ record upto 160 μ s
3. Develop the DAQ system
+ Collect data of 12 FADC boards without network problem

Thanks for your attention