VN-neutrino meeting
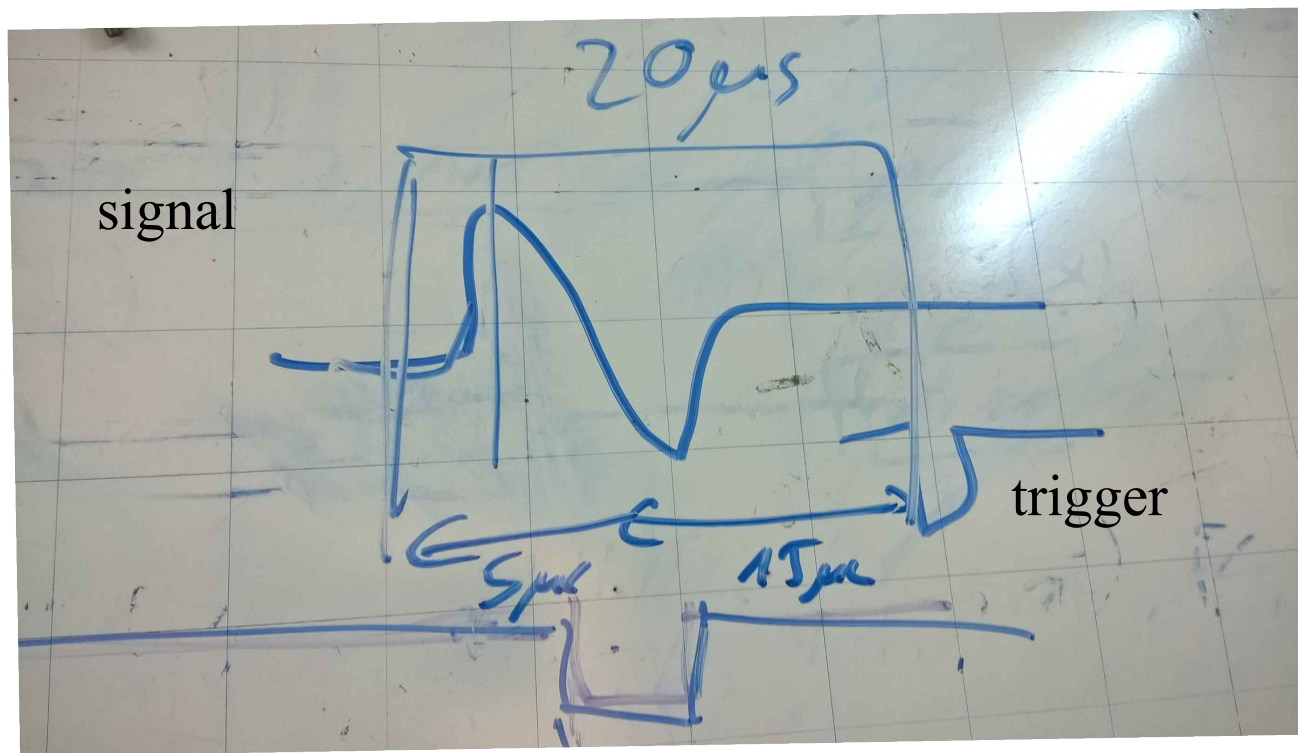
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Dec 01th 2017

Test VMEFADC at HCM

- No VME crate → use power from PC power
- timing:
    + get data in 5 μs
    + signal from trigger 15 μs
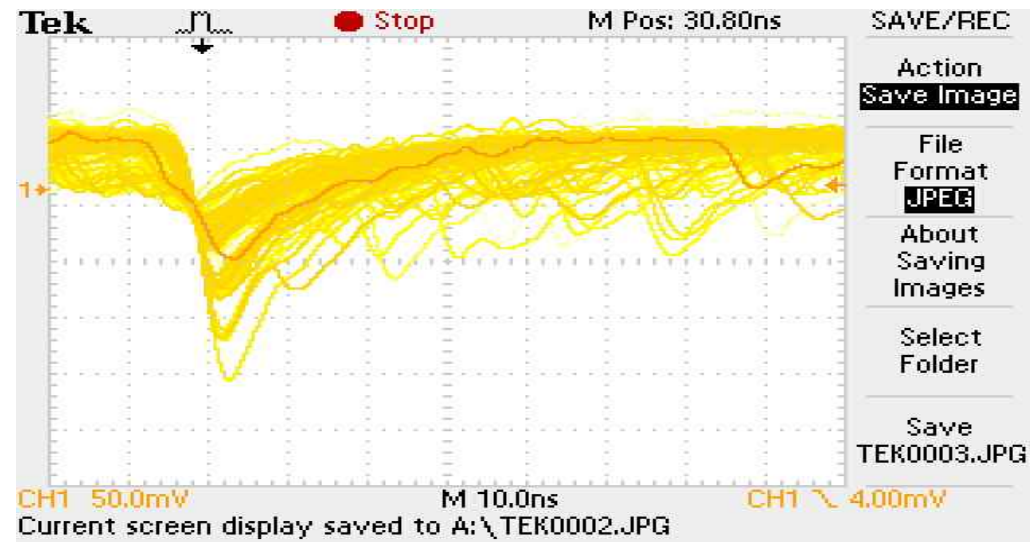
Test MPPC at IFIRSE

- First generation:
  HV 70V
  Amplify 3 times
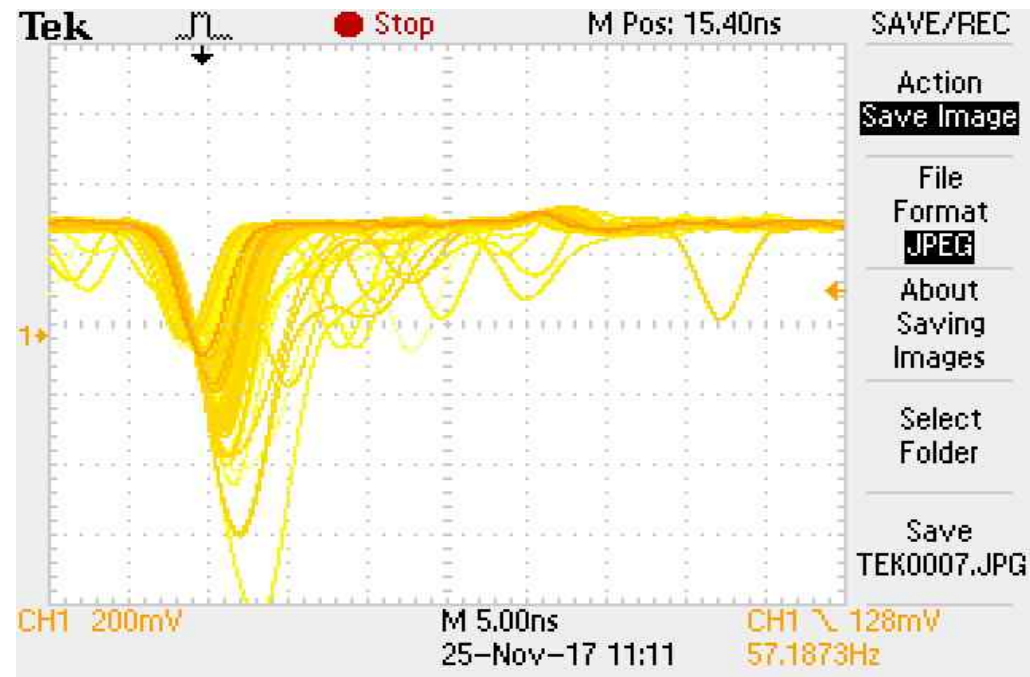 See ~3 photons, after pulse, ...
  Pulse width < 10ns

- Third generation:
  HV 55V
  Amplify 3 times
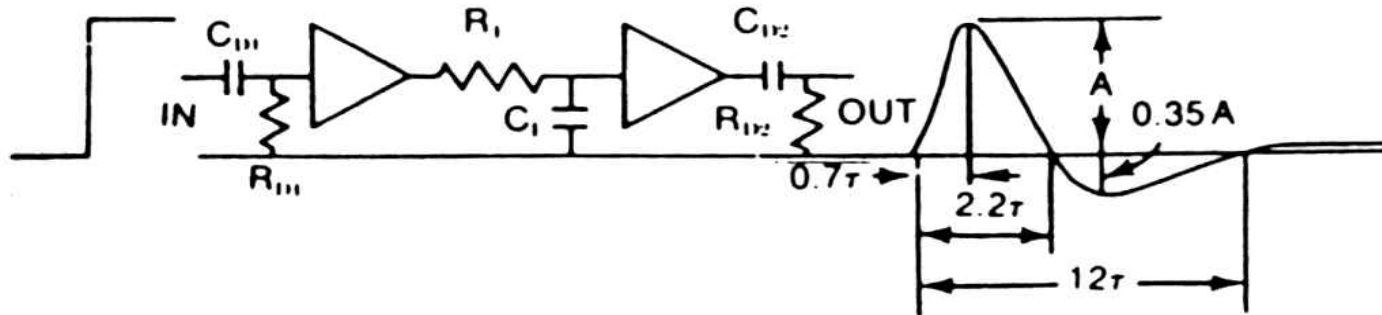 See ~3 photons, after pulse, ...
  Pulse width < 10ns

Can not use VMEFADC with current status

Make CR-RC circuit

- We test mppc with low rate
- increase pulse by cr-rc circuit ?

$$\tau = R_{D1}C_{D1} = R_1C_1 = R_{D2}C_{D2}$$

Nafosted ?

- mppc study ?
- mppc readout ?
- analysis data for WAGASIC/NINJA ?

Thanks for your attention